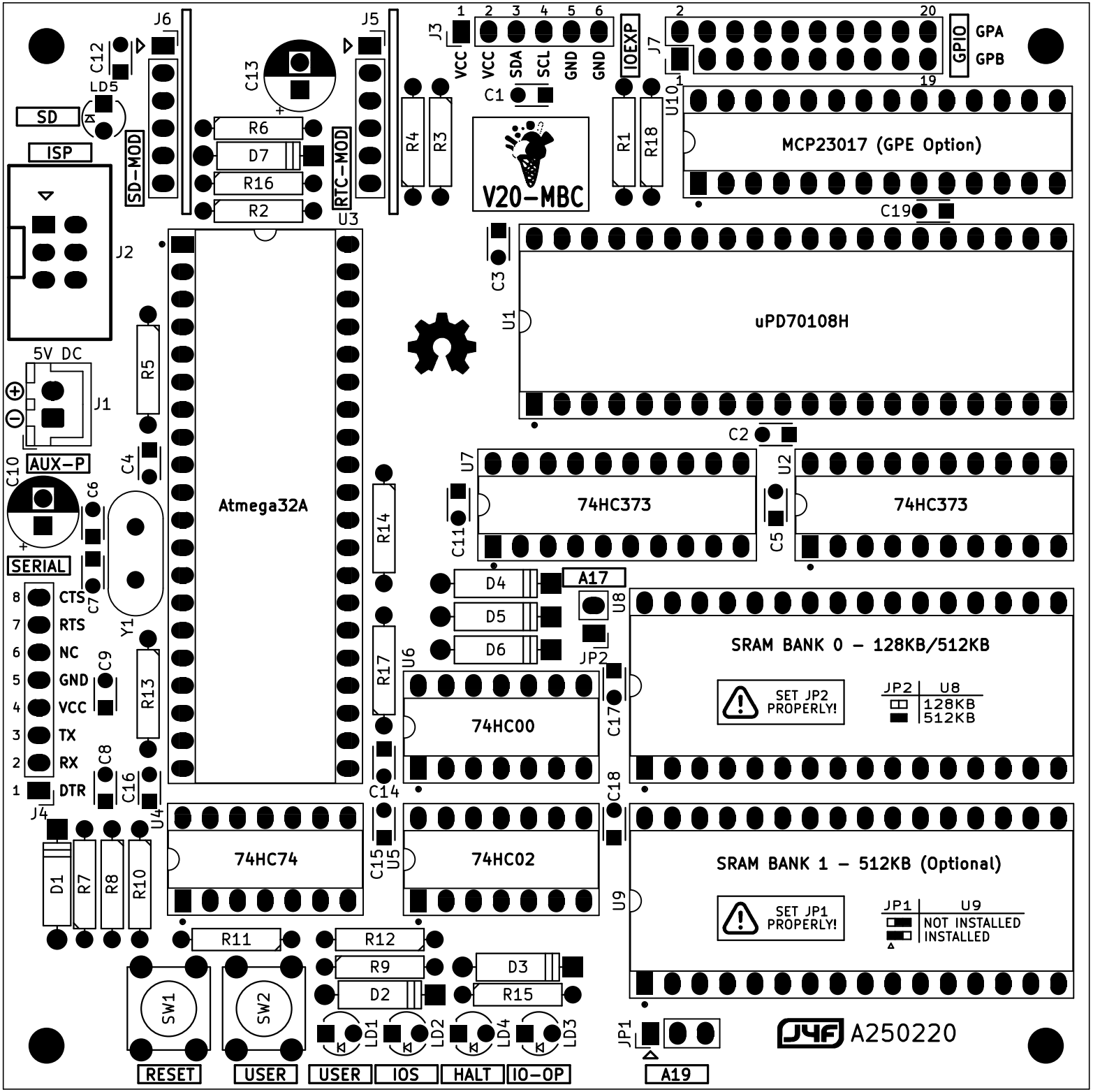
J6
J5
J3
VCC
VCC
SDA
SCL
GND
GND
IOEXP
J7
GPIO
GPA
GPB
C12
LD5
SD
ISP
SD-MOD
C13
R6
D7
R16
R2
RTC-MOD
R4
R3
C1
V20-MBC
R1
R18
U10
MCP23017 (GPE Option)
C19
J2
U3
C3
U1
uPD70108H
5V DC
J1
R5
C4
AUX-P
C10
C6
Atmega32A
R14
C2
U7
74HC373
U2
74HC373
C11
C5
SERIAL
CTS
RTS
NC
GND
VCC
TX
RX
DTR
C7
Y1
C9
R13
C8
C16
J4
D4
D5
D6
A17
U8
JP2
SRAM BANK 0 – 128KB/512KB
SET JP2 PROPERLY!
JP2 U8
128KB
512KB
U6
R17
74HC00
C17
C14
C18
U4
74HC74
C15
U5
74HC02
SRAM BANK 1 – 512KB (Optional)
SET JP1 PROPERLY!
JP1 U9
NOT INSTALLED
INSTALLED
U9
D1
R7
R8
R10
R11
R12
R9
D2
D3
R15
SW1
SW2
LD1
LD2
LD4
LD3
JP1
A250220
RESET
USER
USER
IOS
HALT
IO-OP
A19